

	Document ID	Issue Date	Page s	Title	Current OR
1	US 20060171451 A1	20060803	22	Adaptive equalizer with a dual-mode active taps mask generator and a pilot reference signal amplitude control unit	375/232
2	US 20060153576 A1	20060713	9	Compensation of polarization mode dispersion in single mode fiber for maximum-likelihood sequence estimation	398/158
3	US 20040207763 A1	20041021	21	Digital synchronizing generator	348/723
4	US 20040162050 A1	20040819	16	Method for generating multiplier coefficients for a mixer	455/323
5	US 20030061251 A1	20030327	10	Harmonic-series filter	708/300
6	US 20010056452 A1	20011227	33	Efficient finite field basis conversion involving a dual basis	708/492
7	US 6904443 B2	20050607	9	Harmonic-series filter	708/310
8	US 6466957 B1	20021015	9	Reduced computation system for wavelet transforms	708/300
9	US 6442104 B1	20020827	27	Underwater background-acoustics synthesizer system and method	367/1
10	US 6307903 B1	20011023	33	Low pass digital filter implemented in a modem of a television system	375/350
11	US 6078573 A	20000620	16	Circuitry and method for demodulating code division multiple access (CDMA) signals	370/335
12	US 5396517 A	19950307	23	Transversal filter useable in echo canceler, decision feedback equalizer applications for minimizing non-linear distortion in signals conveyed over full duplex two-wire communication link	375/233
13	US 5268927 A	19931207	13	Digital adaptive transversal filter for spread spectrum receivers	375/147
14	US 4833678 A	19890523	34	Hard-wired serial Galois field decoder	714/781
15	US 4696015 A	19870922	8	Echo correction especially for television broadcast systems	375/232

	<b>Inventor</b>
<b>1</b>	Pietraski; Philip J. et al.
<b>2</b>	Bessios; Anthony
<b>3</b>	Ciardi, John Joseph
<b>4</b>	Menkhoff, Andreas
<b>5</b>	White, Stanley A.
<b>6</b>	Kaliski, Burton S. et al.
<b>7</b>	White; Stanley A.
<b>8</b>	Messerly; Shayne et al.
<b>9</b>	Ridgell, Jr.; James Jefferson et al.
<b>10</b>	Harris; Fred et al.
<b>11</b>	Batalama; Stella N. et al.
<b>12</b>	Yedid; Harry et al.
<b>13</b>	Dimos; George et al.
<b>14</b>	Cohen; Earl T.
<b>15</b>	Palicot; Jacques P. et al.

	Document ID	Issue Date	Page s	Title	Current OR
16	US 4397029 A	19830802	10	Least squares adaptive lattice equalizer	375/231
17	US 4375692 A	19830301	10	Least squares lattice decision feedback equalizer	375/231
18	US 4093994 A	19780606	15	Fast discrete transform generator and digital filter using same	708/400
19	US 3991277 A	19761109	11	Frequency division multiplex system using comb filters	370/484
20	US 3749895 A	19730731	8	APPARATUS FOR SUPPRESSING LIMIT CYCLES DUE TO QUANTIZATION IN DIGITAL FILTERS	708/306
21	US 3648236 A	19720307	8	DECODING METHOD AND APPARATUS FOR BOSE-CHAUDHURI-HOCQUENGHEM CODES	714/782
22	US 20040162050 A	20040819	16	Multiplier coefficient generating method for mixer, involves selecting multiplier group from calculated multiplier set in dependence on predetermined signal/noise ratio of mixer, and writing coefficients into memory of mixer	

	<b>Inventor</b>
<b>16</b>	Satorius; Edgar H. et al.
<b>17</b>	Shensa; Mark J. et al.
<b>18</b>	Nussbaumer; Henri J.
<b>19</b>	Hirata; Yoshimutsu
<b>20</b>	Kao; Chih-Yu
<b>21</b>	Burton; Herbert O.
<b>22</b>	MENKHOFF, A

	Document ID	Issue Date	Pages	Title	Current OR
1	US 20060218470 A1	20060928	24	Multiply redundant raid system and XOR-efficient method and apparatus for implementing the same	714/767
2	US 20060147127 A1	20060706	13	Method and apparatus for electronic image processing	382/298
3	US 20060050074 A1	20060309	19	System and method for representing a general two dimensional spatial transformation	345/427
4	US 20060010430 A1	20060112	37	Device and process for the signature, the marking and the authentication of computer programs	717/127
5	US 20040263786 A1	20041230	28	Metrics to predict subjective impact of eye's wave aberration	351/246
6	US 20040162050 A1	20040819	16	Method for generating multiplier coefficients for a mixer	455/323
7	US 20040154550 A1	20040812	21	Methods and apparatus for a remote, noninvasive technique to detect chronic wasting disease (CWD) and similar diseases in live subjects	119/174
8	US 20040015830 A1	20040122	36	Computational divided differencing	717/104
9	US 20040010742 A1	20040115	37	Method and apparatus for error detection	714/746
10	US 20030192005 A1	20031009	30	Method and apparatus for error detection	714/758
11	US RE39385 E	20061107	30	Method and apparatus for performing mathematical functions using polynomial approximation and a rectangular aspect ratio multiplier	708/490
12	US 6993207 B1	20060131	15	Method and apparatus for electronic image processing	382/298

	<b>Inventor</b>
<b>1</b>	Dickson; Lawrence John
<b>2</b>	Slavin; Keith
<b>3</b>	Bassi; Zorawar S.
<b>4</b>	Cousot; Patrick et al.
<b>5</b>	Williams, David R. et al.
<b>6</b>	Menkhoff, Andreas
<b>7</b>	McQuilkin, Gary L.
<b>8</b>	Reps, Thomas W.
<b>9</b>	Williamson, Clifton James et al.
<b>10</b>	Williamson, Clifton James et al.
<b>11</b>	Brightman; Thomas B. et al.
<b>12</b>	Slavin; Keith

	Document ID	Issue Date	Pages	Title	Current OR
13	US 6657577 B1	20031202	136	Radar plant and measurement technique for determination of the orientation and the depth of buried objects	342/22
14	US 6615387 B1	20030902	40	Method and apparatus for error detection	714/785
15	US 6433643 B1	20020813	21	Reduced latency differentiator	331/1A
16	US 6430588 B1	20020806	61	Apparatus and method for elliptic-curve multiplication and recording medium having recorded thereon a program for implementing the method	708/492
17	US 6219815 B1	20010417	17	High-speed syndrome calculation	714/781
18	US 6128417 A	20001003	31	Image partition moment operators	382/288
19	US 6124542 A	20000926	21	Wavefunction sound sampling synthesis	84/603
20	US 6082856 A	20000704	15	Methods for designing and making contact lenses having aberration control and contact lenses made thereby	351/160H
21	US 6061826 A	20000509	17	Hardware-optimized reed-solomon decoder for large data blocks	714/784
22	US 6058500 A	20000502	19	High-speed syndrome calculation	714/781
23	US 6041431 A	20000321	30	Method and apparatus for performing error correction code operations	714/784
24	US 6026420 A	20000215	10	High-speed evaluation of polynomials	708/492
25	US 5905740 A	19990518	25	Apparatus and method for error correction	714/784
26	US 5834672 A	19981110	7	Non-linear tone generator	84/660
27	US 5323402 A	19940621	20	Programmable systolic BCH decoder	714/782
28	US 5157538 A	19921020	8	Silicon spatial light modulator	359/245

	<b>Inventor</b>
<b>13</b>	Gregersen; Ole et al.
<b>14</b>	Williamson; Clifton James et al.
<b>15</b>	Opsahl; Paul L. et al.
<b>16</b>	Kobayashi; Tetsutaro et al.
<b>17</b>	DesJardins; Philip A. et al.
<b>18</b>	Ausbeck, Jr.; Paul J.
<b>19</b>	Wang; Avery L.
<b>20</b>	Dunn; Stephen A. et al.
<b>21</b>	Thirumoorthy; Hari et al.
<b>22</b>	DesJardins; Philip A. et al.
<b>23</b>	Goldstein; Arthur M.
<b>24</b>	DesJardins; Philip A. et al.
<b>25</b>	Williamson; Clifton J.
<b>26</b>	Wang; Avery L.
<b>27</b>	Vaccaro; John J. et al.
<b>28</b>	Soref; Richard A.



	Document ID	Issue Date	Pages	Title	Current OR
29	US 5042001 A	19910820	23	Method and apparatus for performing mathematical functions using polynomial approximation and a rectangular aspect ratio multiplier	708/490
30	US 4967340 A	19901030	16	Adaptive processing system having an array of individually configurable processing components	712/19
31	US 4852098 A	19890725	14	Polynomial operator in galois fields and a digital signal processor comprising an operator of this type	714/782
32	US 4833678 A	19890523	34	Hard-wired serial Galois field decoder	714/781
33	US 4759000 A	19880719	12	Acoustic energy absorbing material	367/176
34	US 4599722 A	19860708	35	Apparatus for encoding and decoding digital data to permit error correction	714/776
35	US 4544230 A	19851001	16	Method of evaluating a polynomial function using an array of optical modules	385/2
36	US 4544229 A	19851001	17	Apparatus for evaluating a polynomial function using an array of optical modules	359/285
37	JP 2006059085 A	20060302	12	DATA PROCESSOR, DATA PROCESSING METHOD, AND PROCESSING PROGRAM	
38	WO 2005069775 A	20050804	12	Reed-Solomon encoding method used in communication device, involves appending sum of products to data block as parity symbols to form codeword	
39	SU 1140115 A	19850215	5	Systems n-th degree polynomial evaluator - has argument switched from memory to both inputs of multiplier at input to register with output switched to second multiplier	
40	SU 744559 B	19800630	7	Arbitrary coefft. degree polynomial evaluator - has AND=gate with inputs from control unit, carry counter and from first digit place of register	

	<b>Inventor</b>
<b>29</b>	Brightman; Thomas B. et al.
<b>30</b>	Dawes; Robert L.
<b>31</b>	Brechard; Dominique et al.
<b>32</b>	Cohen; Earl T.
<b>33</b>	Reitz; Ronald P.
<b>34</b>	Mortimer; Brian C.
<b>35</b>	Caulfield; Henry J.
<b>36</b>	Verber; Carl M.
<b>37</b>	UMINAGA, MASAHIRO et al.
<b>38.</b>	GLOSSNER, J et al.
<b>39</b>	VILENSKII, G B
<b>40</b>	CHUVATIN, A N et al.